PTO
DEC 29 2003
PATENT & TRADEMARK



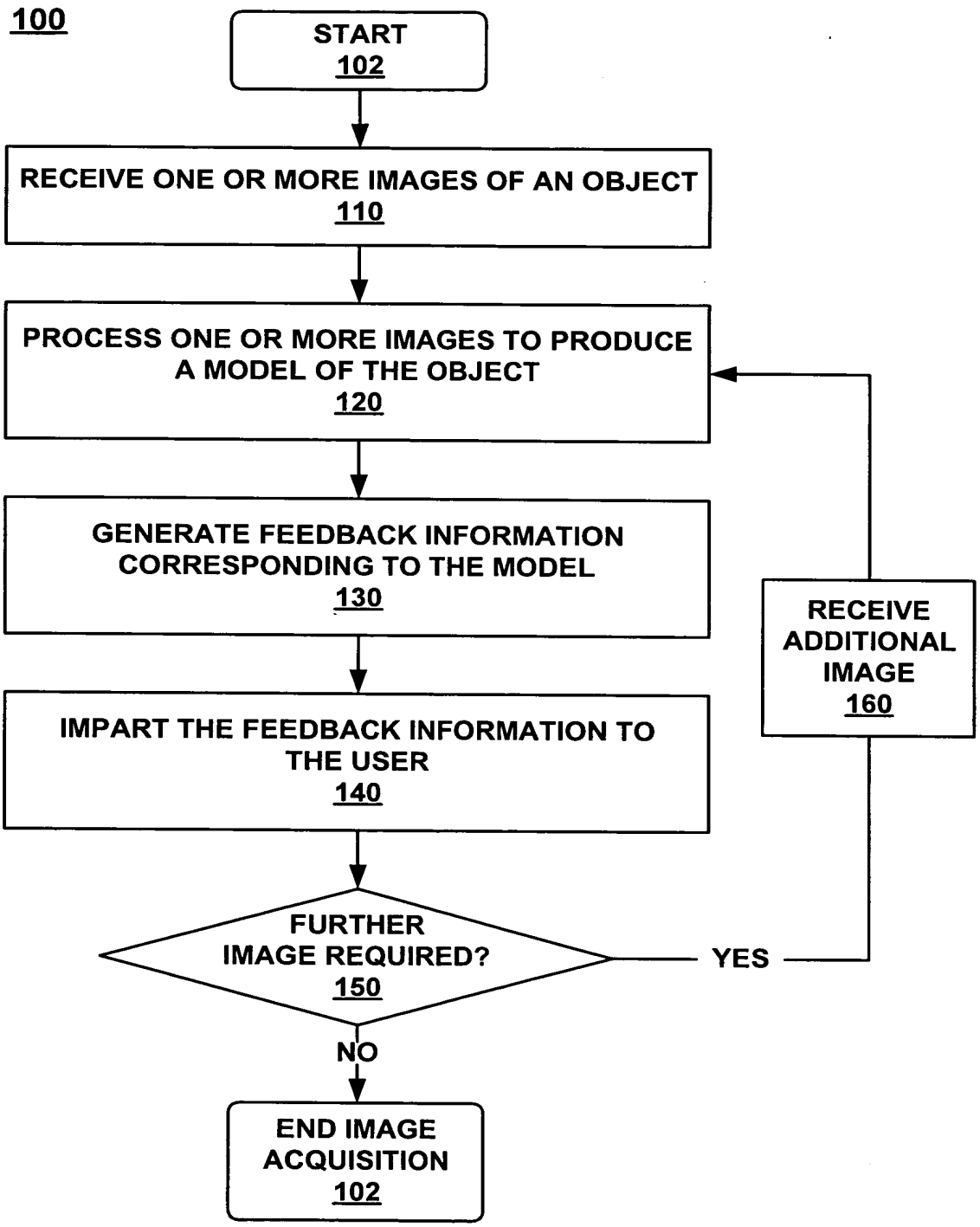


FIG. 1

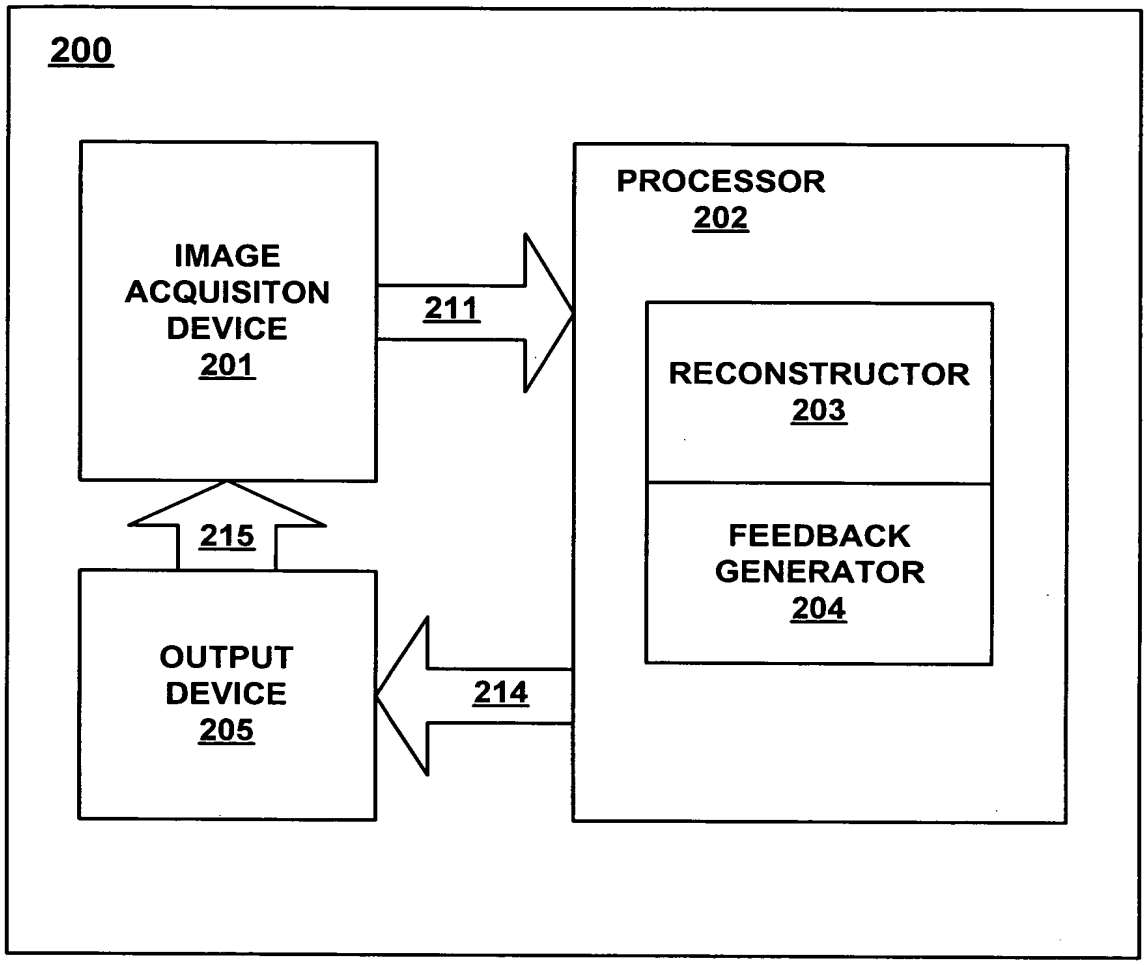
200
IMAGE ACQUISITON DEVICE 201
211
PROCESSOR 202
RECONSTRUCTOR 203
FEEDBACK GENERATOR 204
215
OUTPUT DEVICE 205
214

FIG. 2A

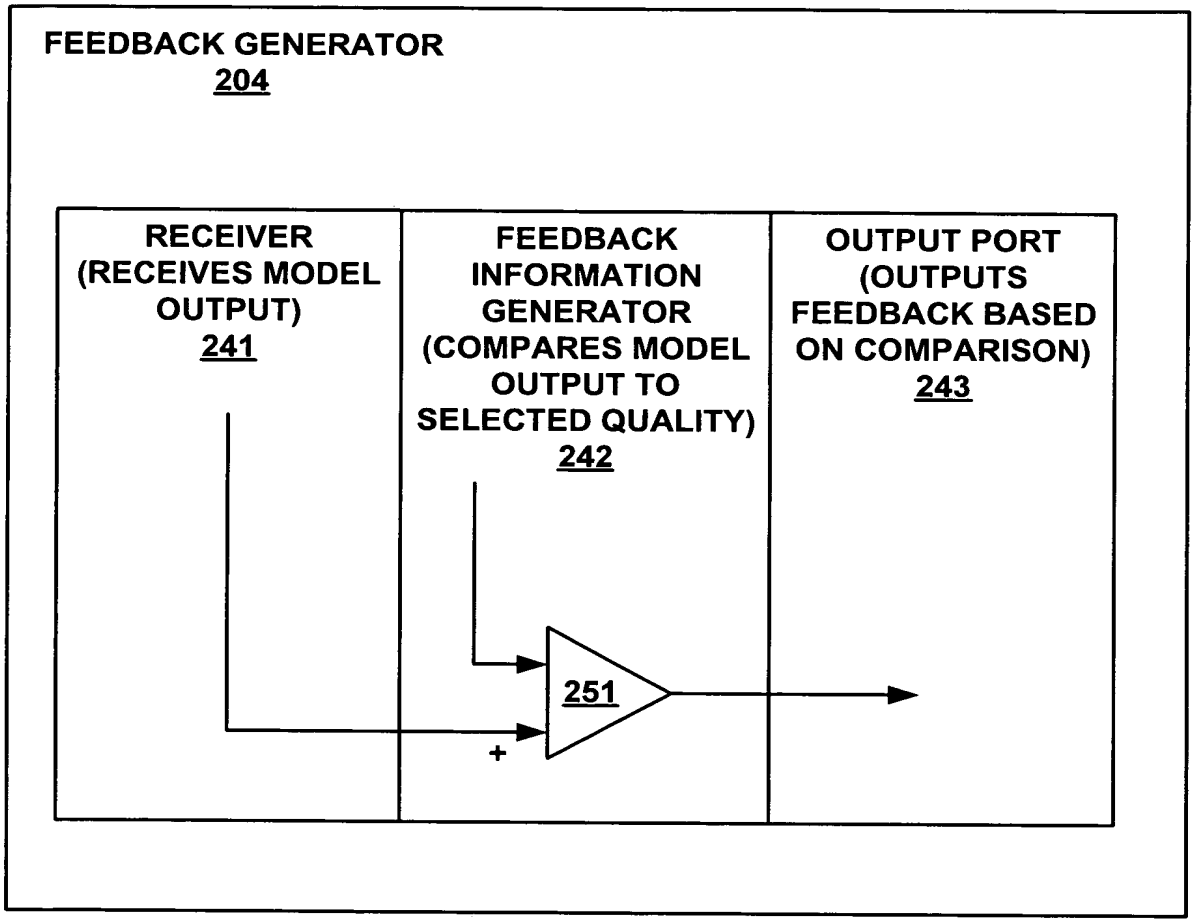
FEEDBACK GENERATOR
204
RECEIVER (RECEIVES MODEL OUTPUT)
241
FEEDBACK INFORMATION GENERATOR (COMPARES MODEL OUTPUT TO SELECTED QUALITY)
242
OUTPUT PORT (OUTPUTS FEEDBACK BASED ON COMPARISON)
243
251
+

FIG. 2B

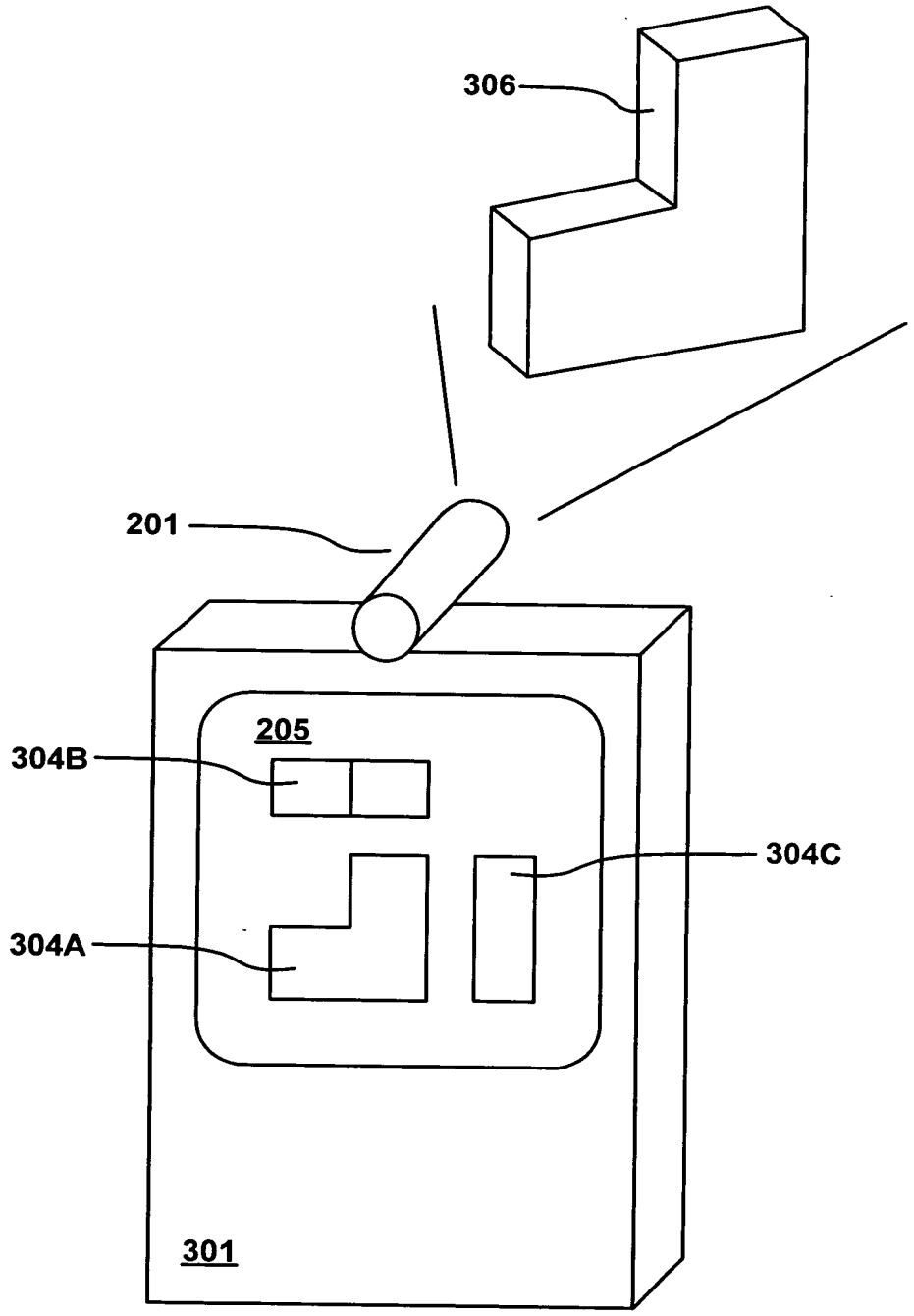


FIG. 3A

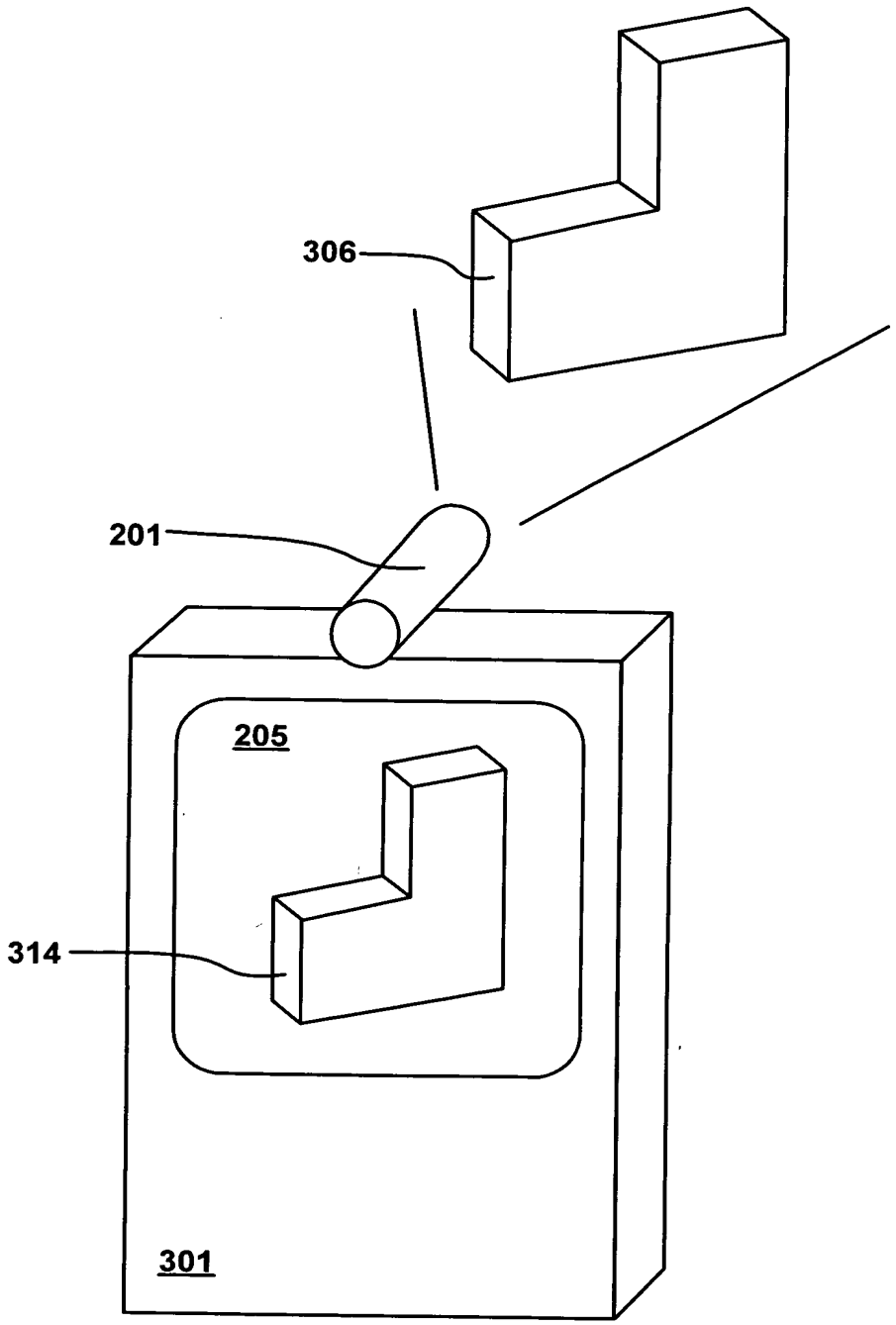


FIG. 3B

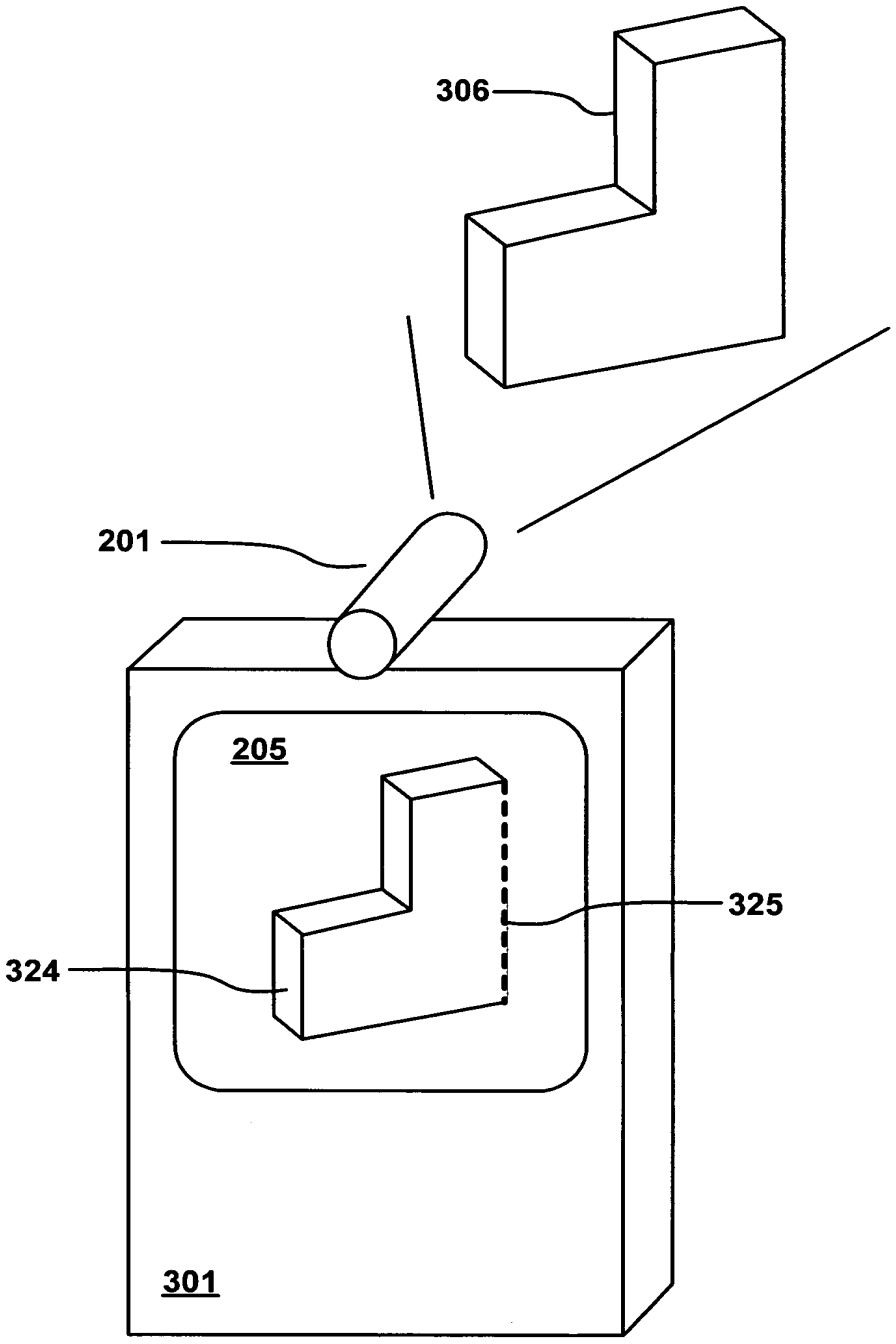


FIG. 3C

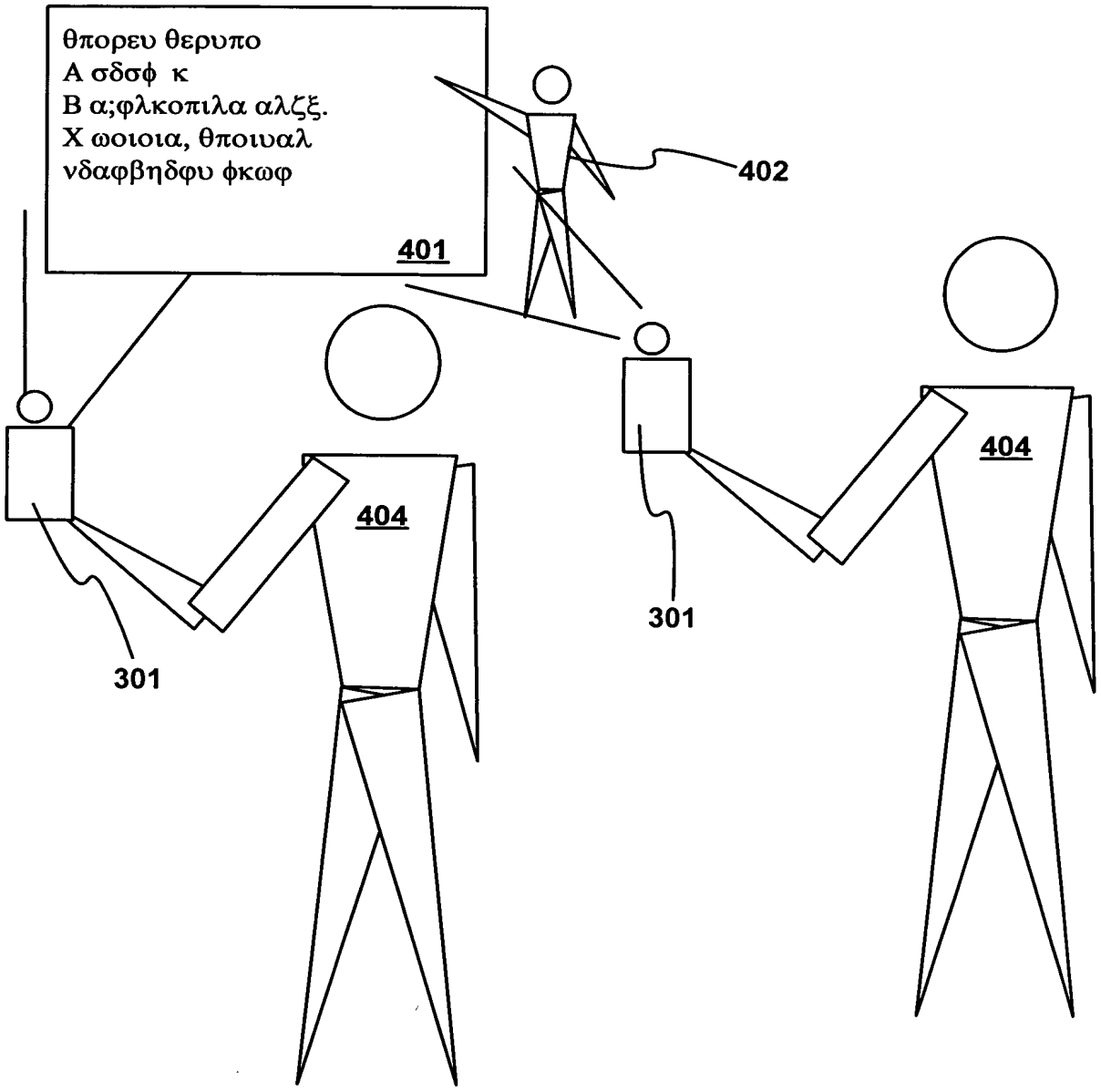
θπορευ θερυπο
A σδσφ κ
B α;φλκοπιλα αλζξ.
X ωοιοια, θποιυαλ
νδαφβηδφυ φκωφ
401
402
404
404
301
301

FIG. 4A

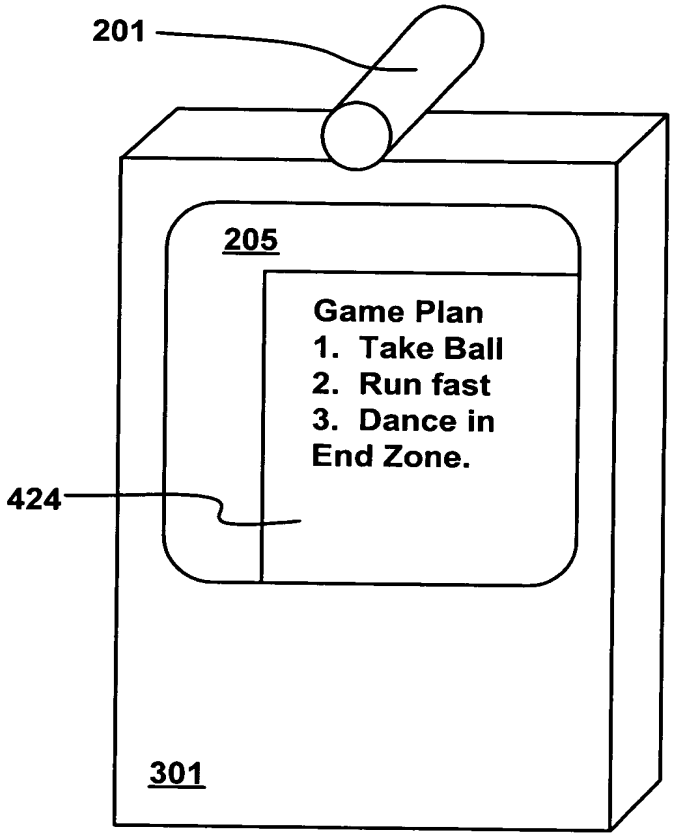
201
205
Game Plan
1. Take Ball
2. Run fast
3. Dance in
End Zone.
424
301

FIG. 4B

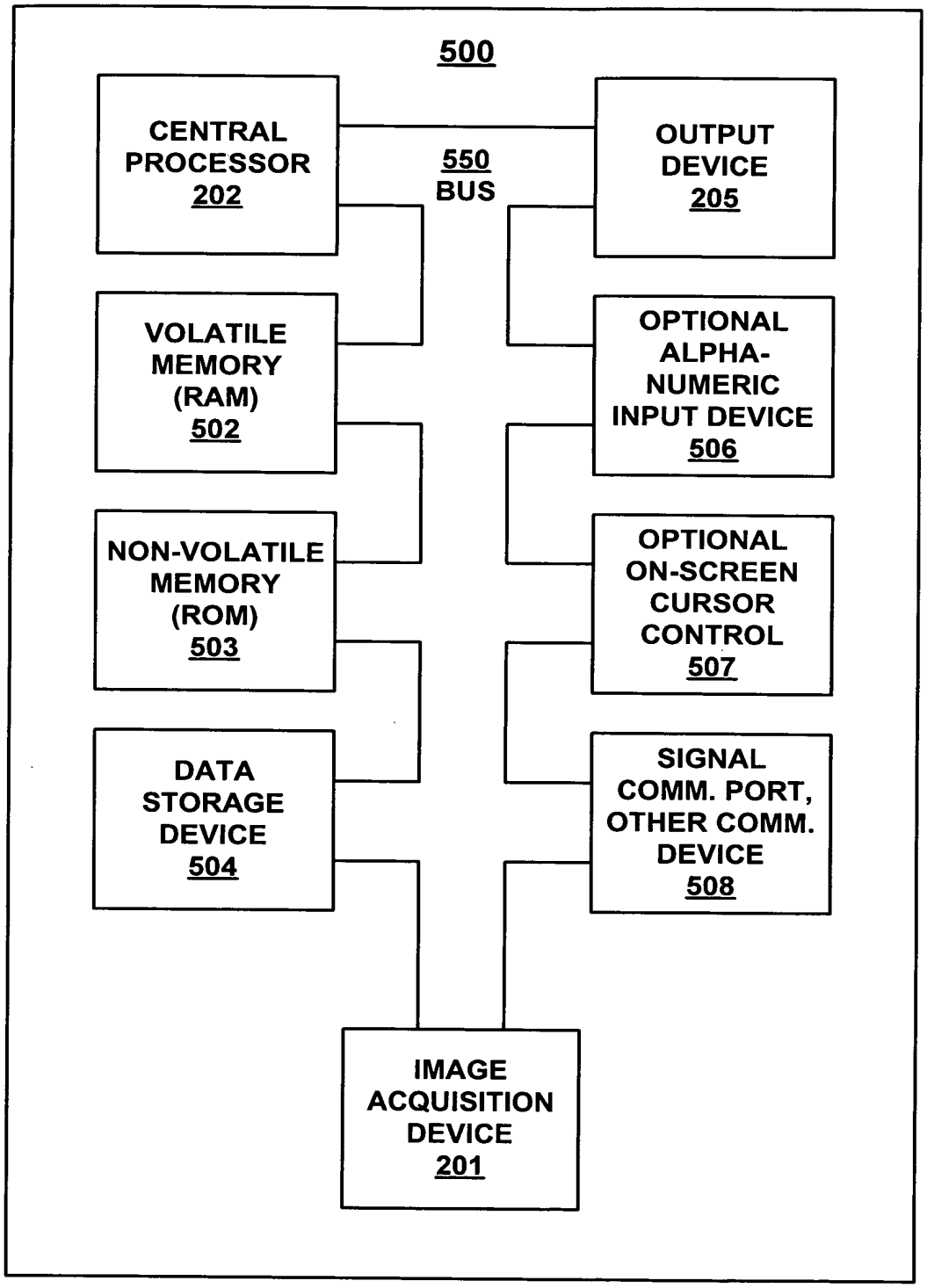
500
CENTRAL PROCESSOR 202
550 BUS
OUTPUT DEVICE 205
VOLATILE MEMORY (RAM) 502
OPTIONAL ALPHA-NUMERIC INPUT DEVICE 506
NON-VOLATILE MEMORY (ROM) 503
OPTIONAL ON-SCREEN CURSOR CONTROL 507
DATA STORAGE DEVICE 504
SIGNAL COMM. PORT, OTHER COMM. DEVICE 508
IMAGE ACQUISITION DEVICE 201

FIG. 5